

MS-6570 Specification

CPU: AMD K7 Serial Socket 462

North Bridge: Nvidia Crush 17/18

South Bridge: Nvidia MCP/2

On Board PCI Device: Serial ATA

On Board Device: RealTek AC97 Codec

On Board Device: IEEE1394 PHY

On Board Device: ICS MII LAN

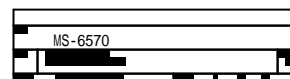
On Board DDR SDRAM: X3

On Board AGP Slot: X1

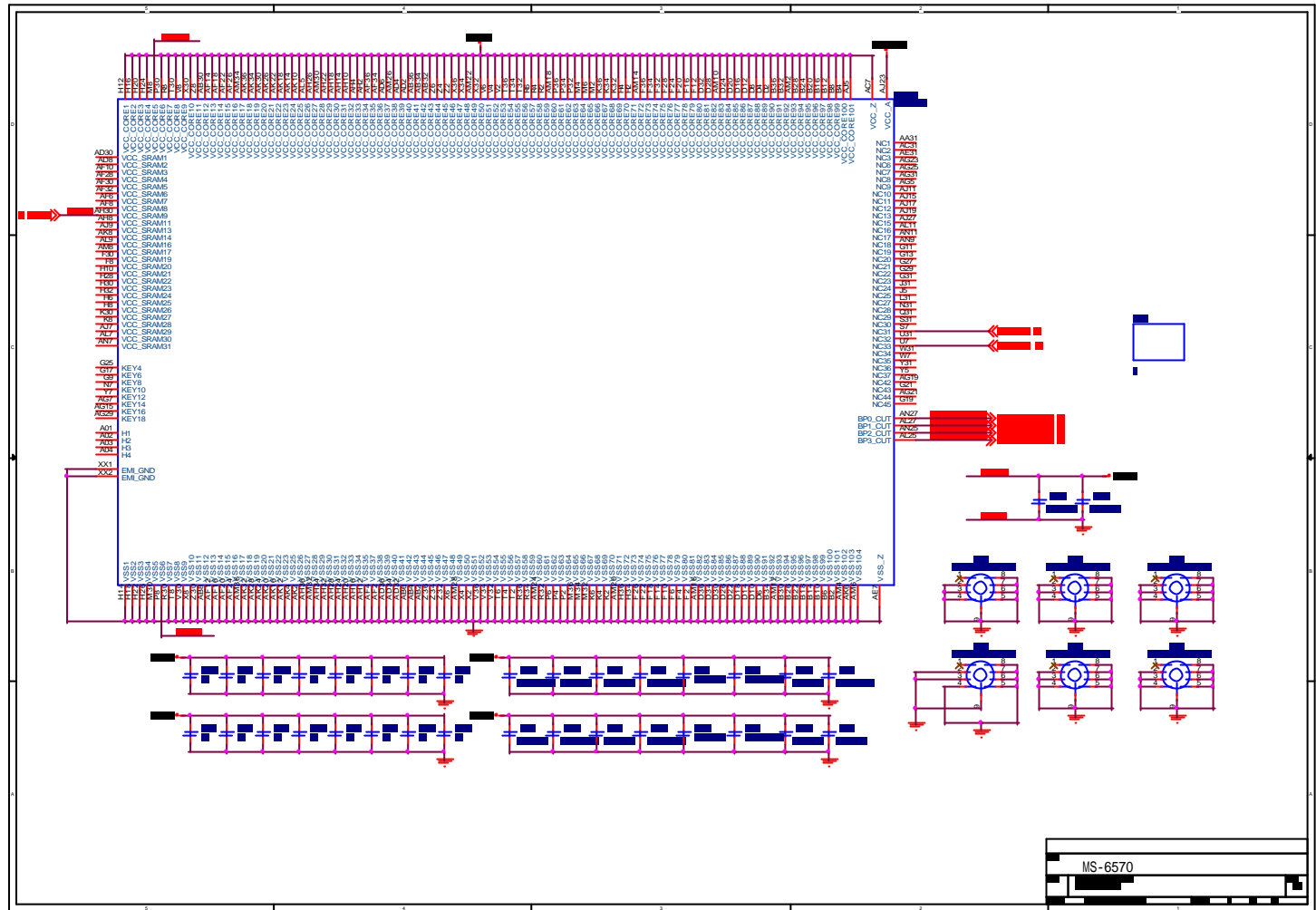
On Board PCI Slot: X5

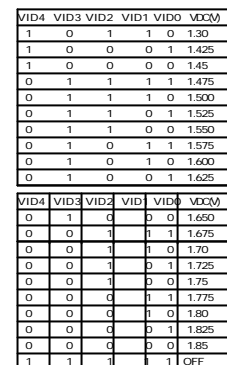
On Board ACR Slot: X1

GPIO and PCI BUS Address









NOTE: INSIDE ON CRU SOCKET

```

Rubycon 2200uF/6.3V capacitor
Rated voltage: 6.3V
Surge voltage: 8V
Leakage current: 415uA/2min
DF= 0.24@120Hz
Ripple current= 2.55A(max)
Vendor guaranteed: 3000 Hr/105 degree.C
Actual operating temperature: 55 degree.C @45A
Ambient temperature: 25 degree.C
實際壽命 =  $L1 \cdot 2^{(T1-T2)/10} \cdot 10^{(Ta-T2)/K}$ 
K = 10, if ripple current within specification
K = 5, if ripple current over specification
實際壽命 =  $L1 \cdot 2^{(T1-T2)/10}$ 
實際壽命 =  $3000 \cdot 2^{(105-55)/10}$ 
實際壽命 = 96000 Hr

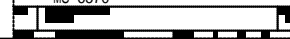
```

Ripple current of Vender's design:

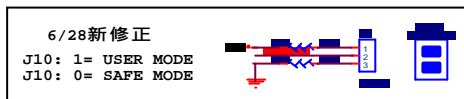
$$I_{ac} = \frac{\Delta(T) \cdot B \cdot A \cdot W_C}{\tan \phi}$$

Delta(T): 溫升 ; $A = 0.785 \cdot \{ [D(D+4L)] / 10 \}$
 B: 散熱係數 ; $WC = 6.28 \cdot f \cdot C$
 D: 電容外徑 ; L: 電容長度
 $\tan\delta = R/X_c$;
DF (電容損失角) = $\tan\delta \cdot 100\%$

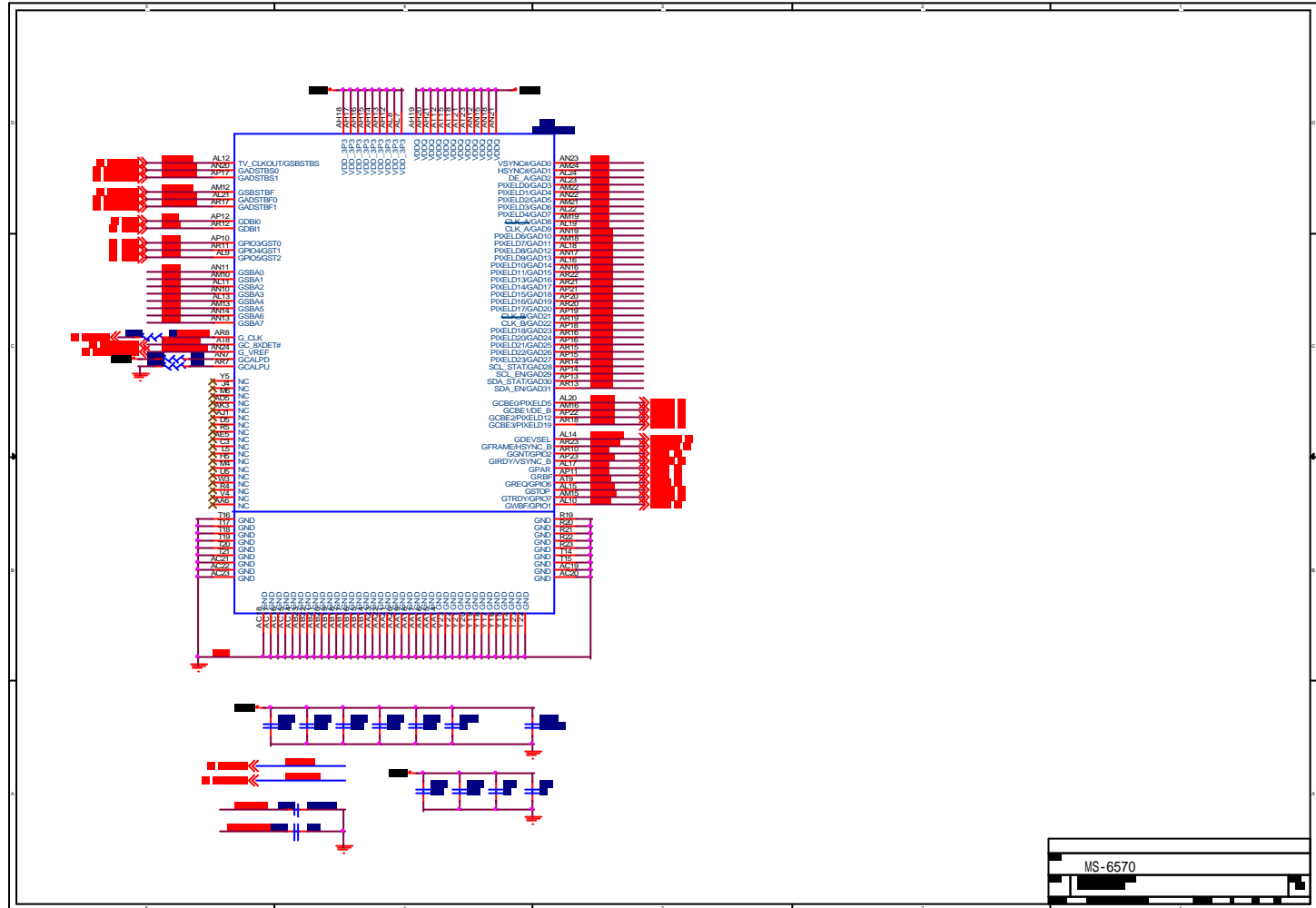
MS-6570

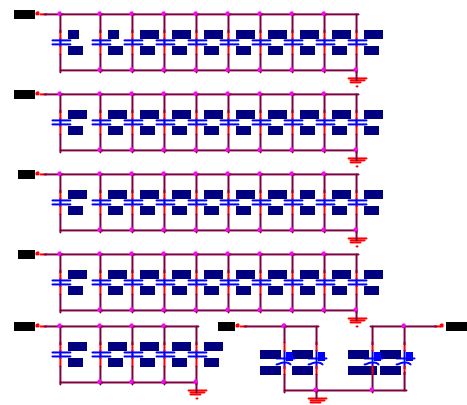
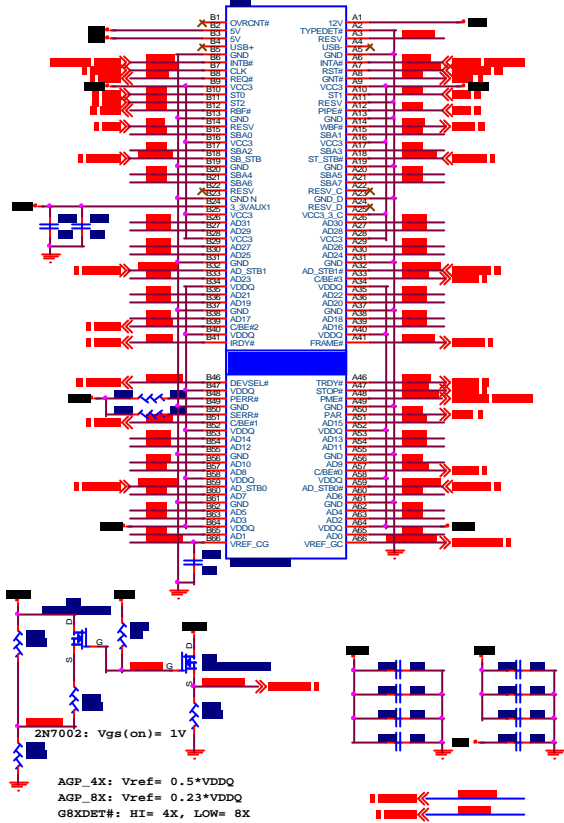




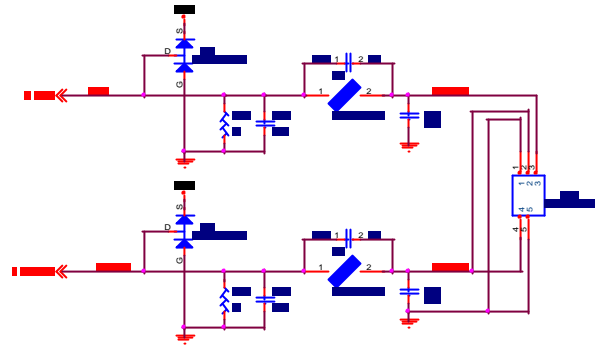


MS-6570





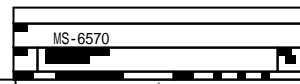
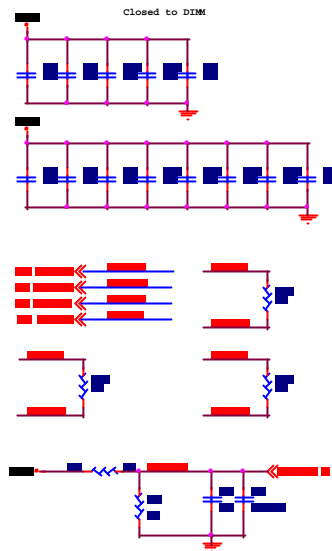
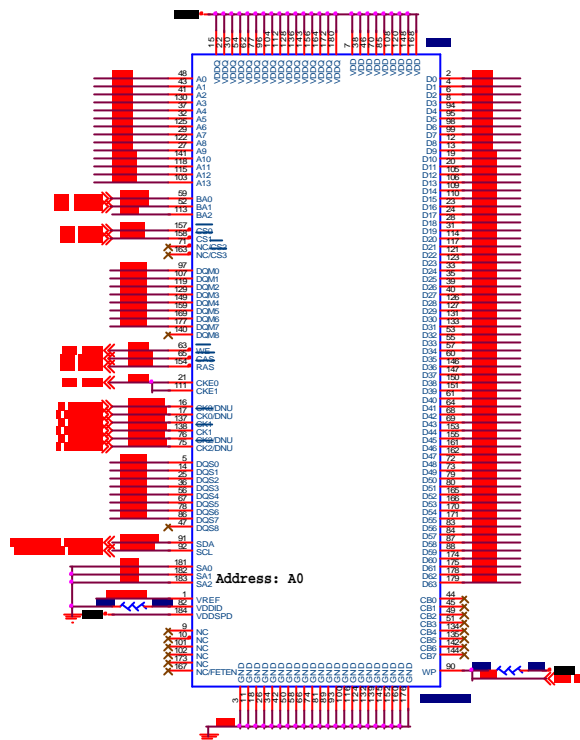
These Capacitance closed to PCI Slot

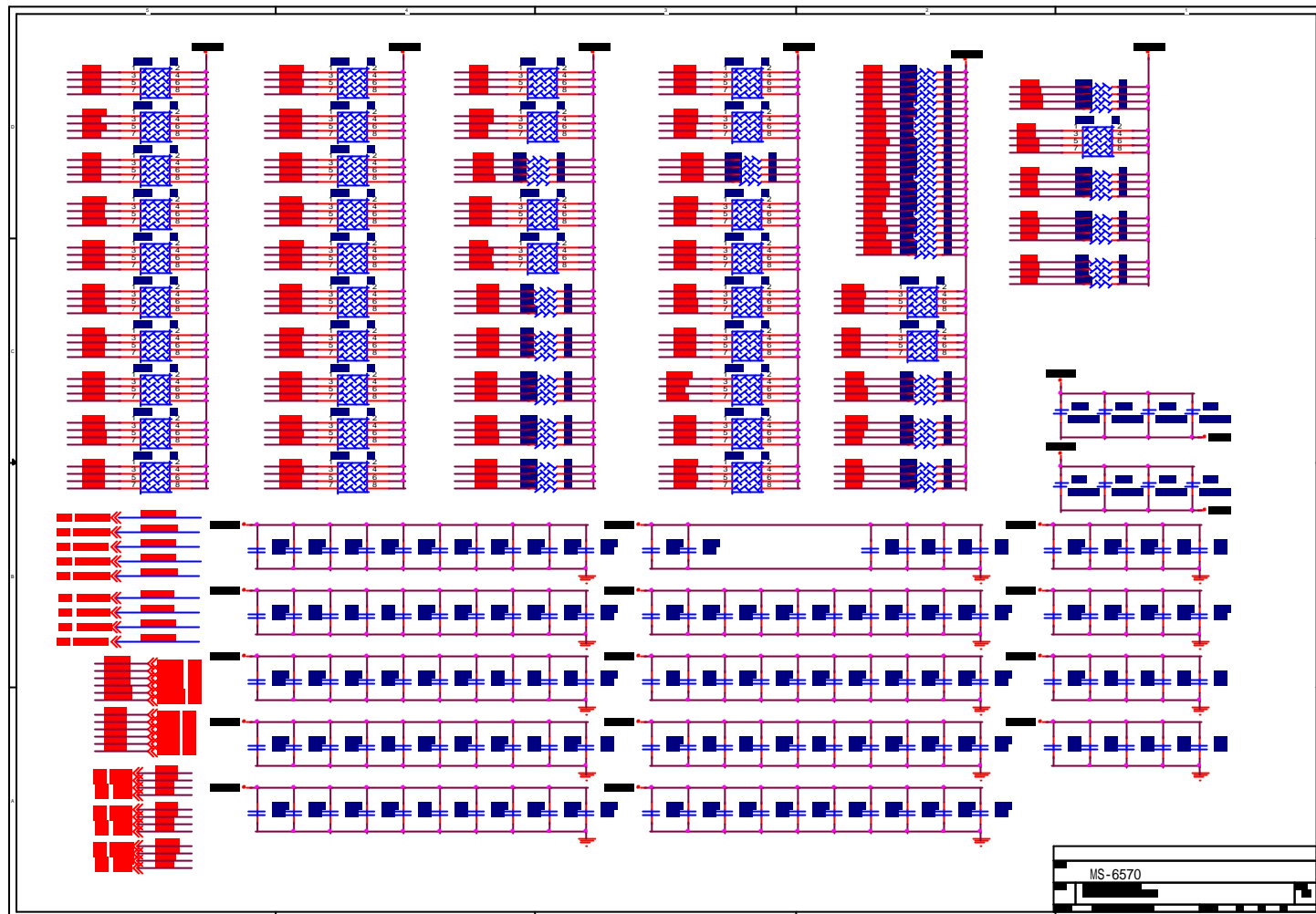


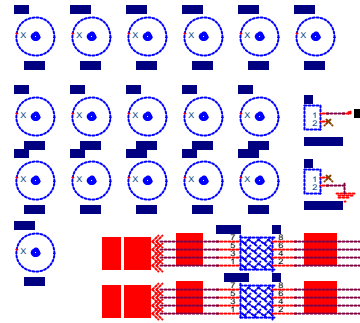
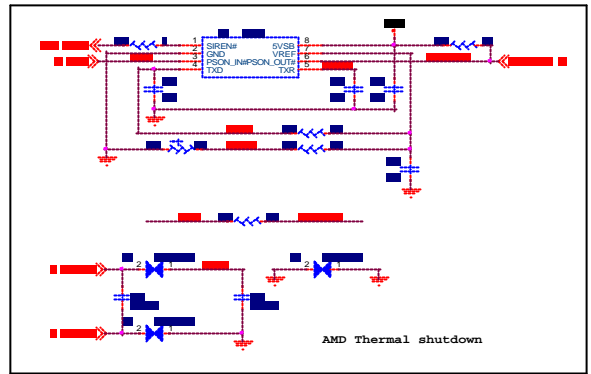
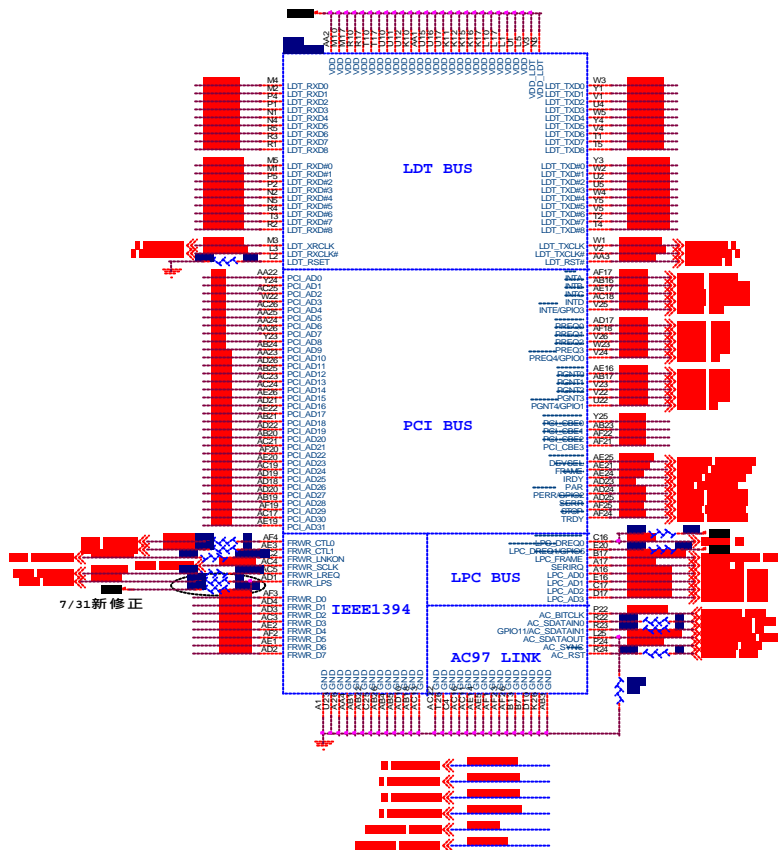
TV OUT Circuit

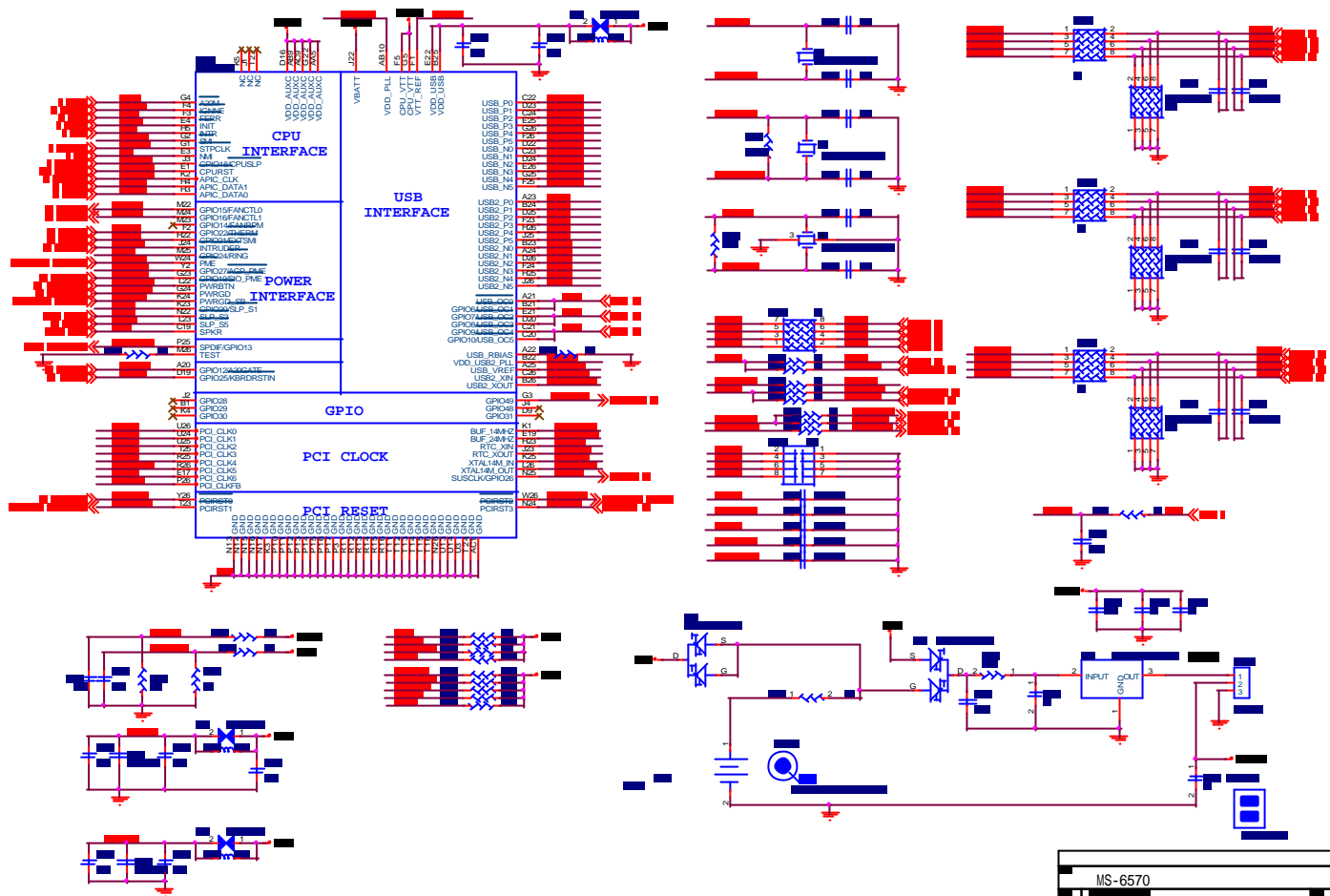
MS-6570

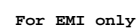
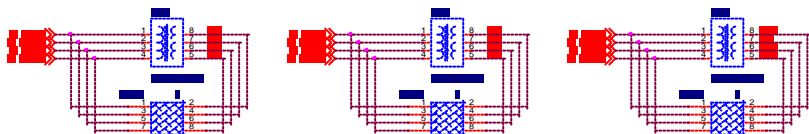




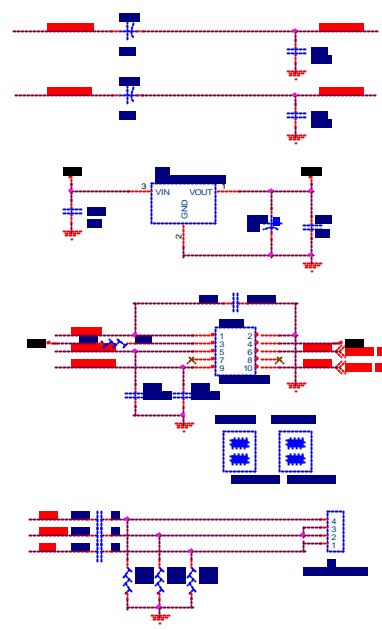
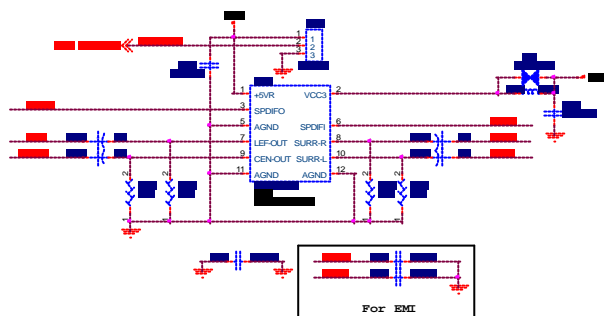
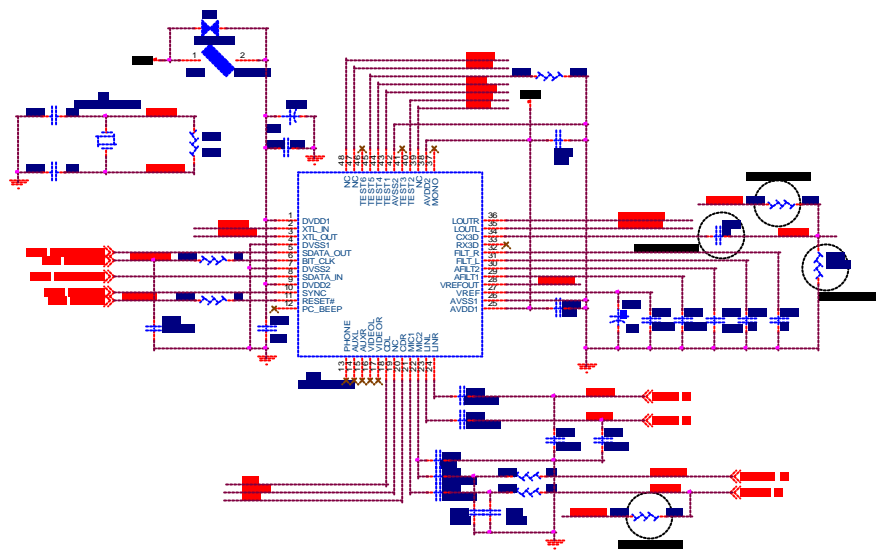




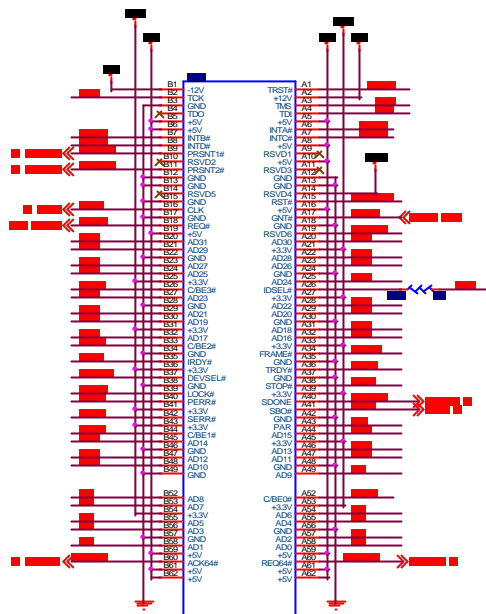
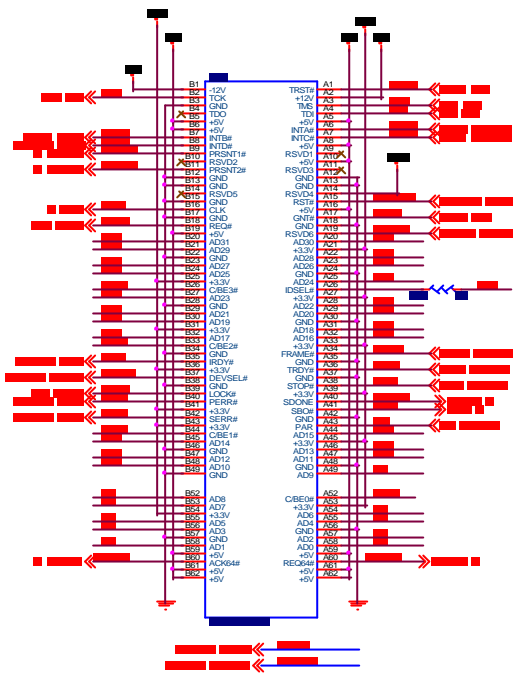




MS-6570

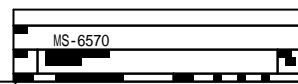


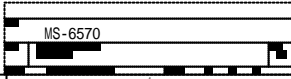
MS-6570

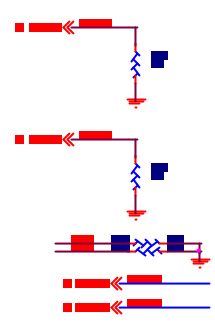
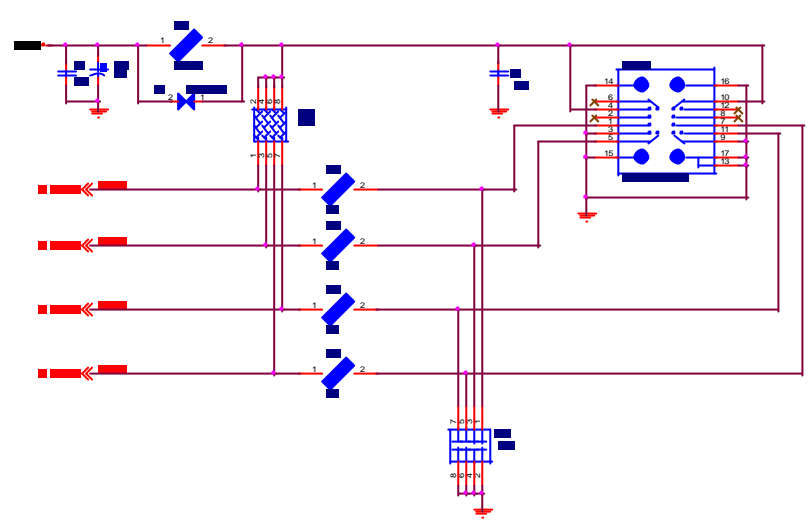
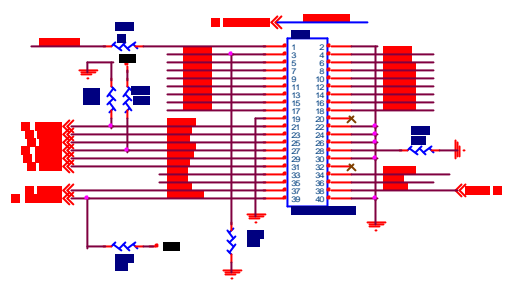
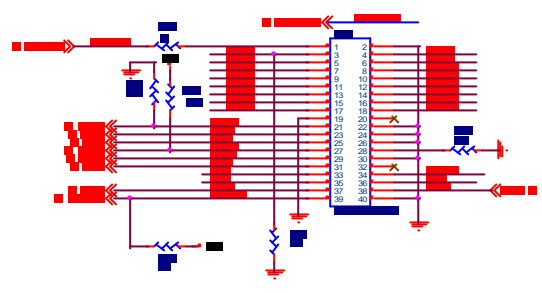


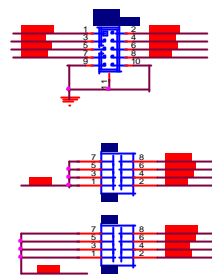
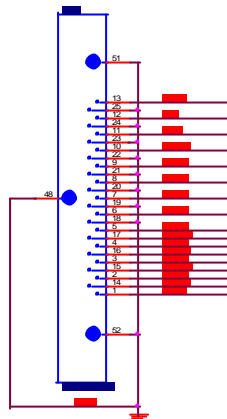
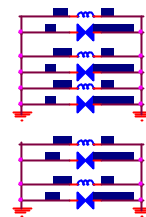
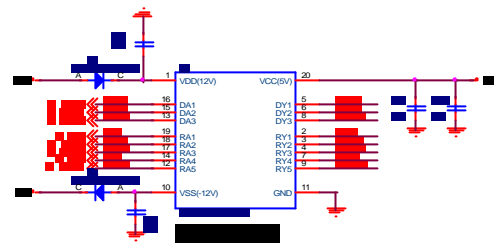
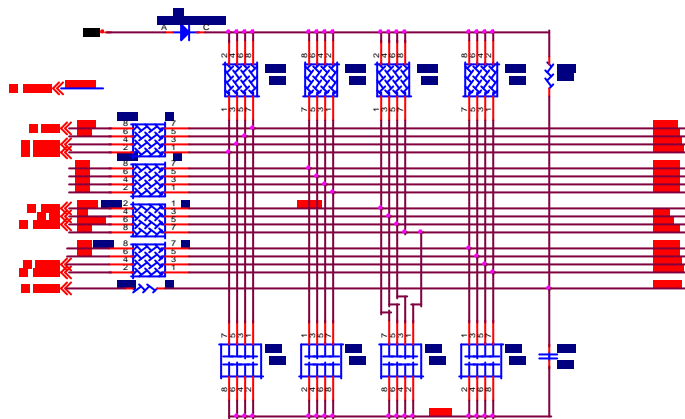
MS-6570

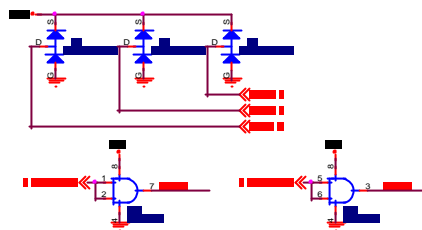
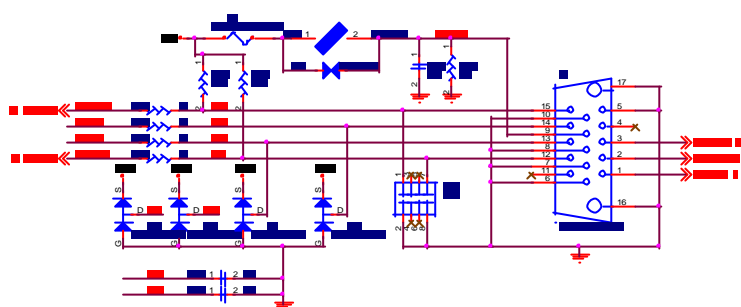
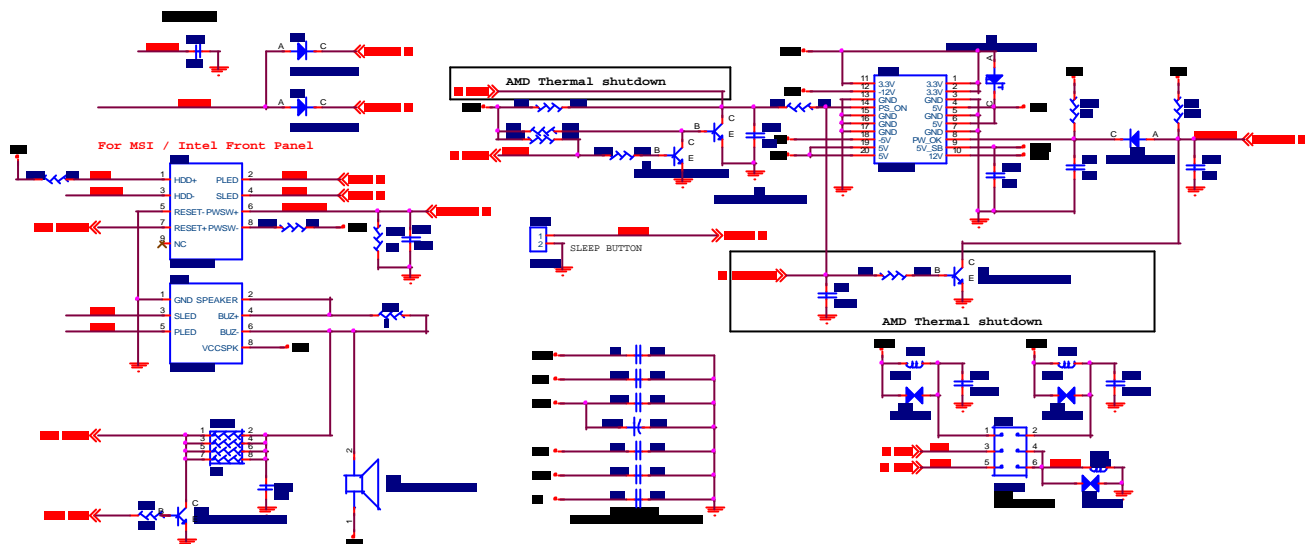












MS-6570

DATE	DESCRIPTION
4/16	1. First version initiated.

DATE	DESCRIPTION

DATE	DESCRIPTION
7/5 Rev.0B	Changed HSDIN#[0..1] form pull_down to pull_up Vcore
	Added J10 for user/safe mode selection
	Changed R203/R207 from pull_down to pull_up
	Added R507 where connected to VCCA_PLL
	Used VCCPLL_NB to control PWROK_SB delay
	Added pull_up resistor R526/R527 for PERR#/SERR# of AGP slot
	Swap APICD[0..1] for correct connection
	Added comm chock for USB 2.0
	IEEE1394 power circuit modified

