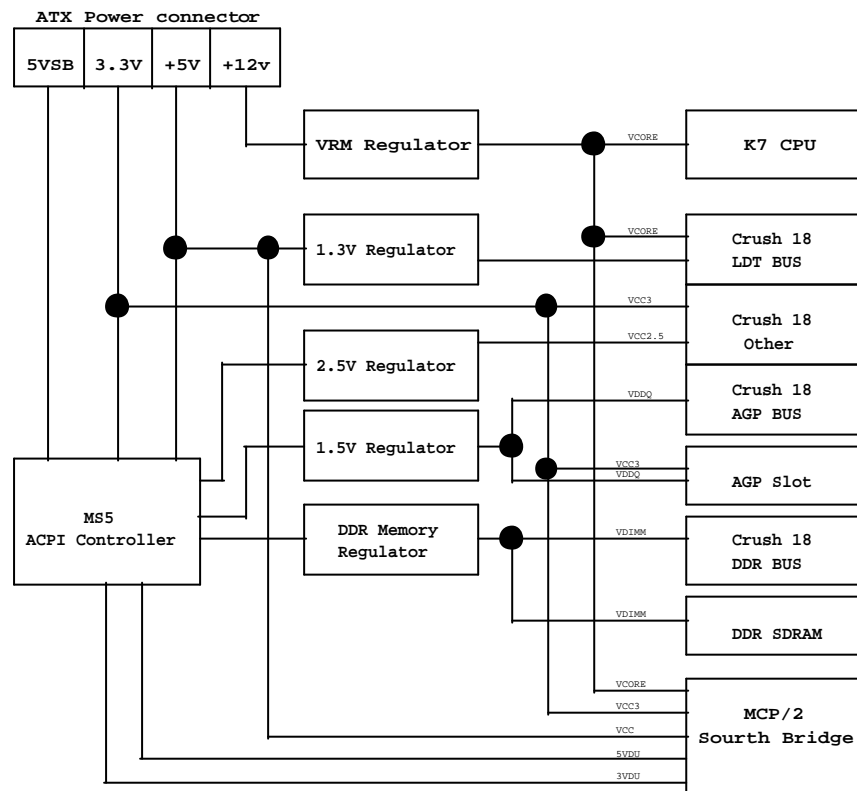


## MS-6570 Version: 0D

Powered by  
Venson.Chen

003-K7 CPU Part 1  
004-K7 CPU Part 2  
005-VRM 9.0  
006-Crush17/18 Part 1  
007-Crush17/18 Part 2  
008-Crush17/18 Part 3  
009-Crush17/18 Part 4  
Power OK Circuit  
010-AGP Slot & TV-out  
011-DDR DIMM1 & 2  
012-DDR DIMM3  
013-DDR Terminator Resistor  
014-MCP/2 Part 1  
015-MCP/2 Part 2  
016-MCP/2 Part 3  
017-ACR Slot & MS3 & BIOS  
018-MII LAN & USB Connector  
019-IEEE1394 & VGA Connector  
020-Realtek 650 AC97 codec  
021-PCI Slot1 & 2  
022-PCI Slot3 & 4  
023-PCI Slot5 & MS1  
024-On board Serial ATA  
025-K/M Connector & IDE Connector  
026-COM/Print Port  
027-W83627HF LPC SIO & Game Port  
028-MS5 ACPI Controller & Power  
029-ATX Power & Front Panel  
030-History

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## MS-6570 Specification

CPU: AMD K7 Serial Socket 462

North Bridge: Nvidia Crush 17/18

South Bridge: Nvidia MCP/2

On Board PCI Device: Serial ATA

On Board Device: RealTek AC97 Codec

On Board Device: IEEE1394 PHY

On Board Device: ICS MII LAN

On Board DDR SDRAM: X3

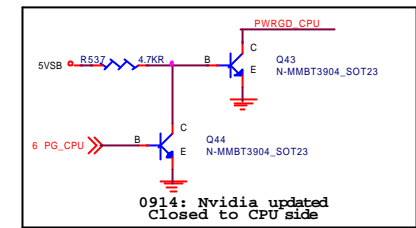
On Board AGP Slot: X1

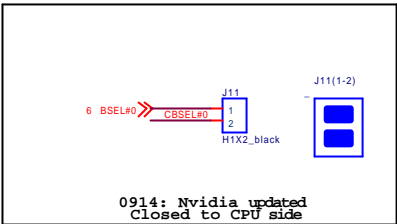
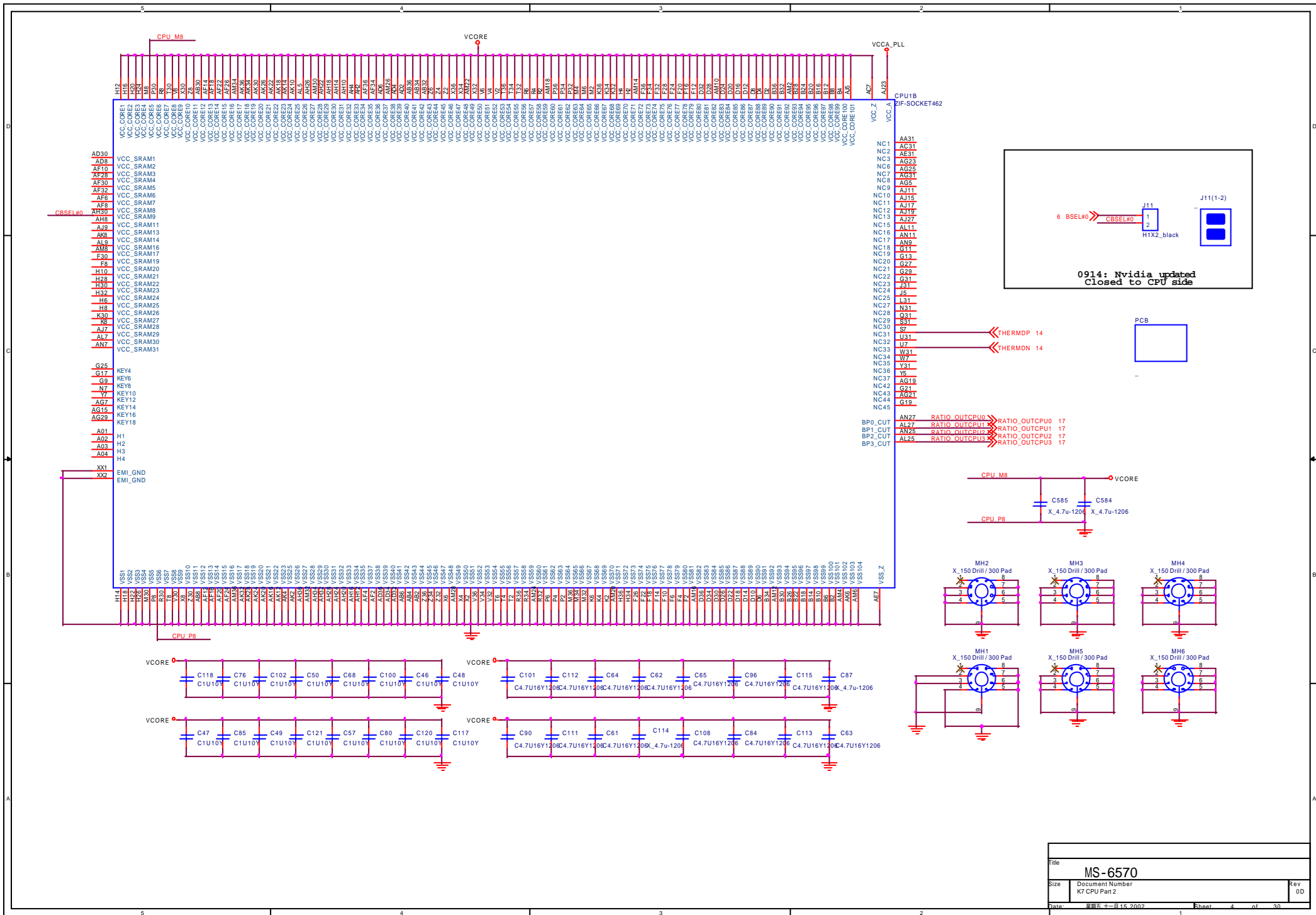
On Board PCI Slot: X5

On Board ACR Slot: X1

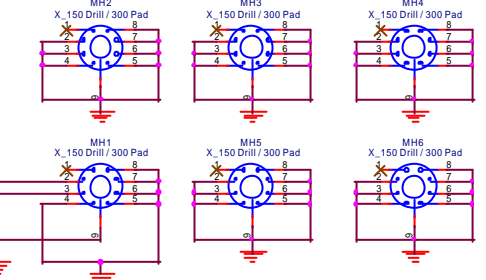
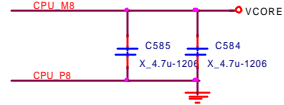
## GPIO and PCI BUS Address

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0914: Nvidia updated  
Closed to CPU side

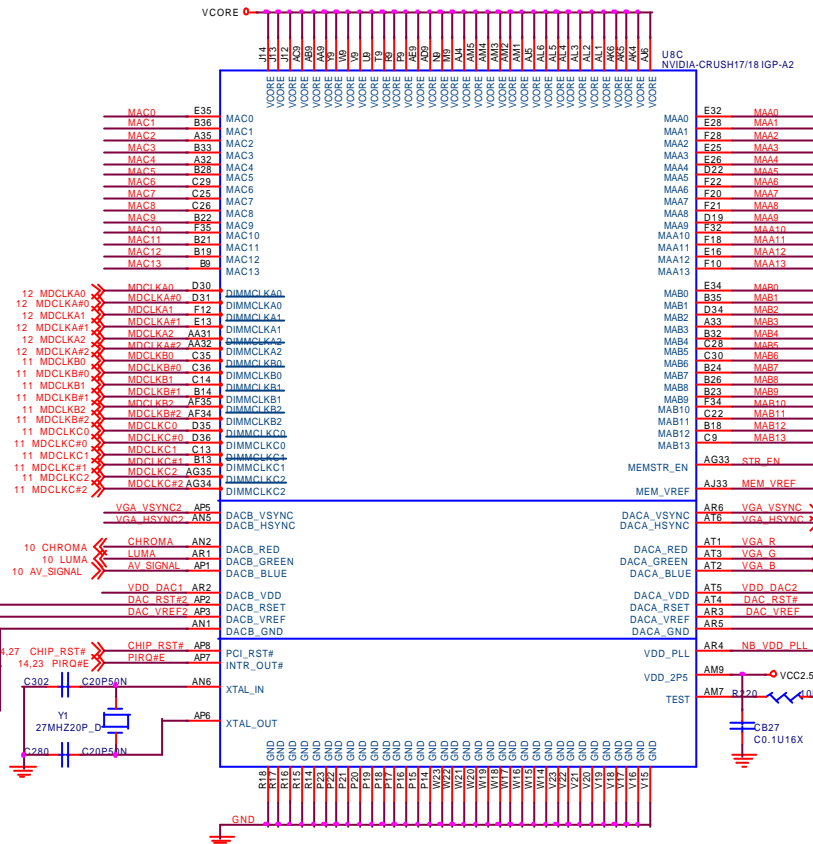


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	K7 CPU Part 2	0D
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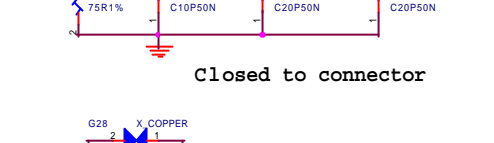
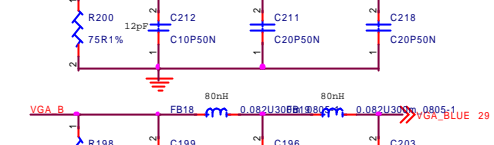
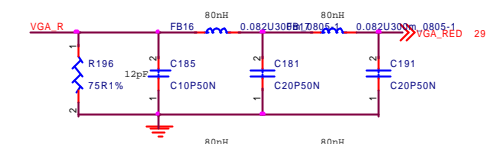
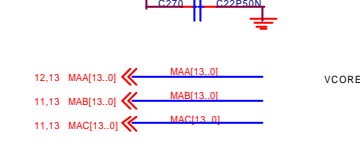
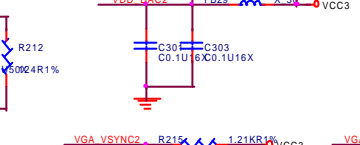
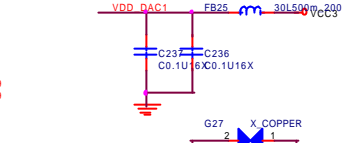
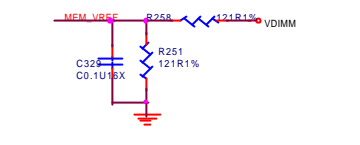
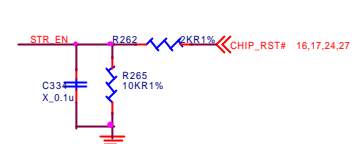
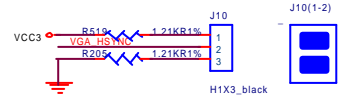




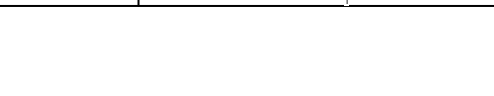
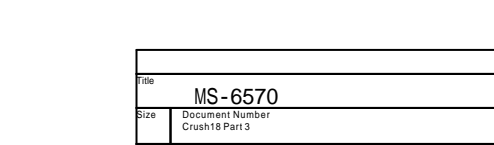
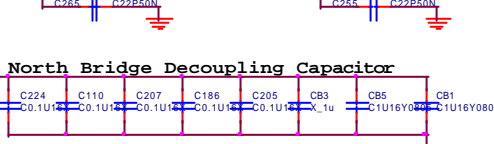
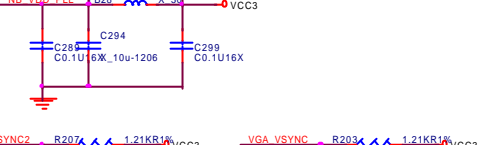




J10: 1= USER MODE  
J10: 0= SAFE MODE

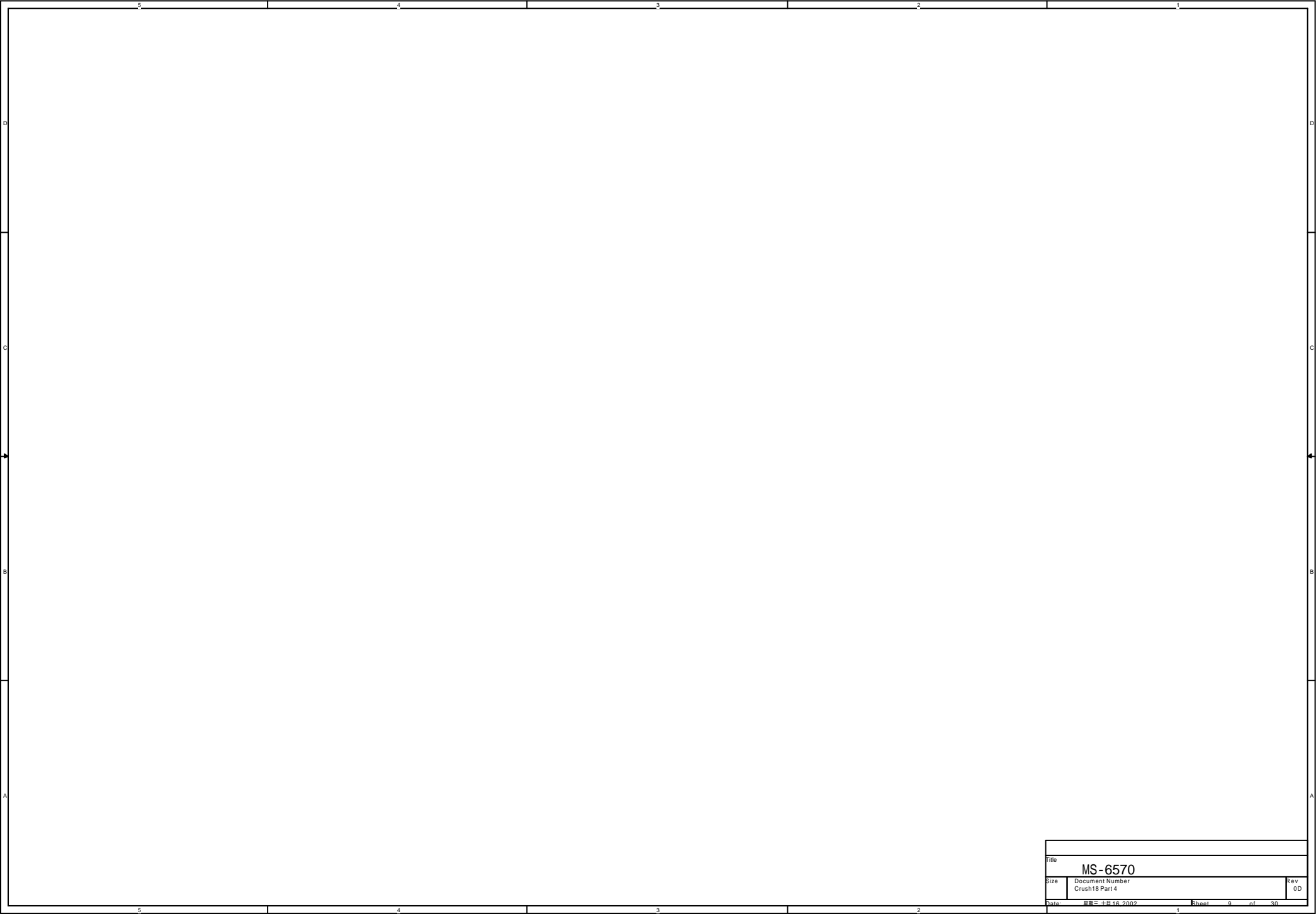


Closed to connector



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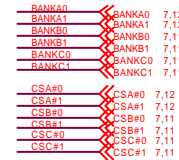
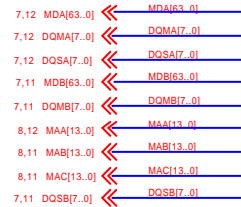
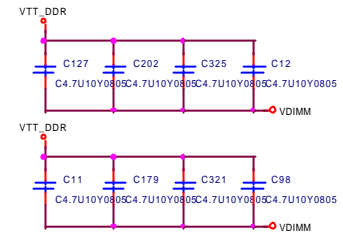




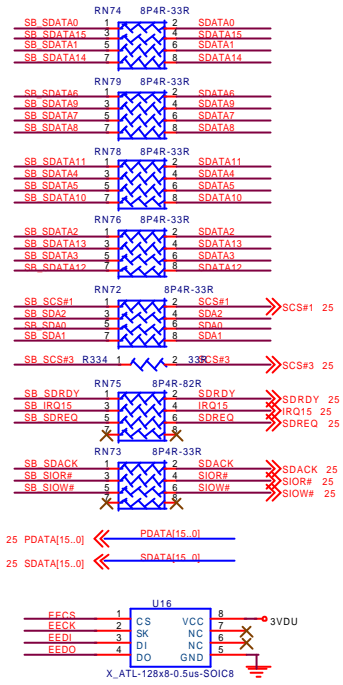
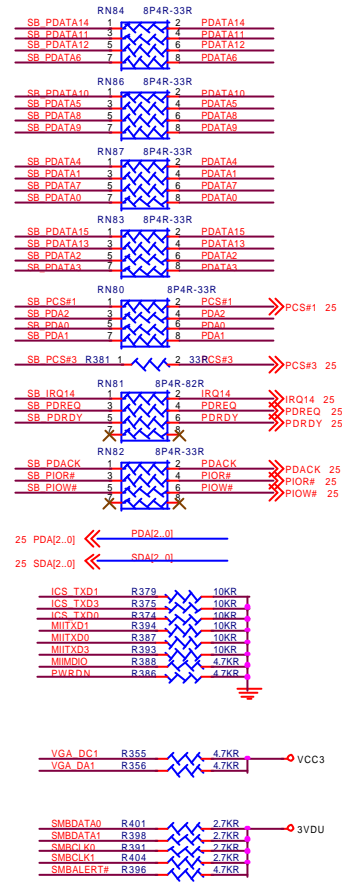
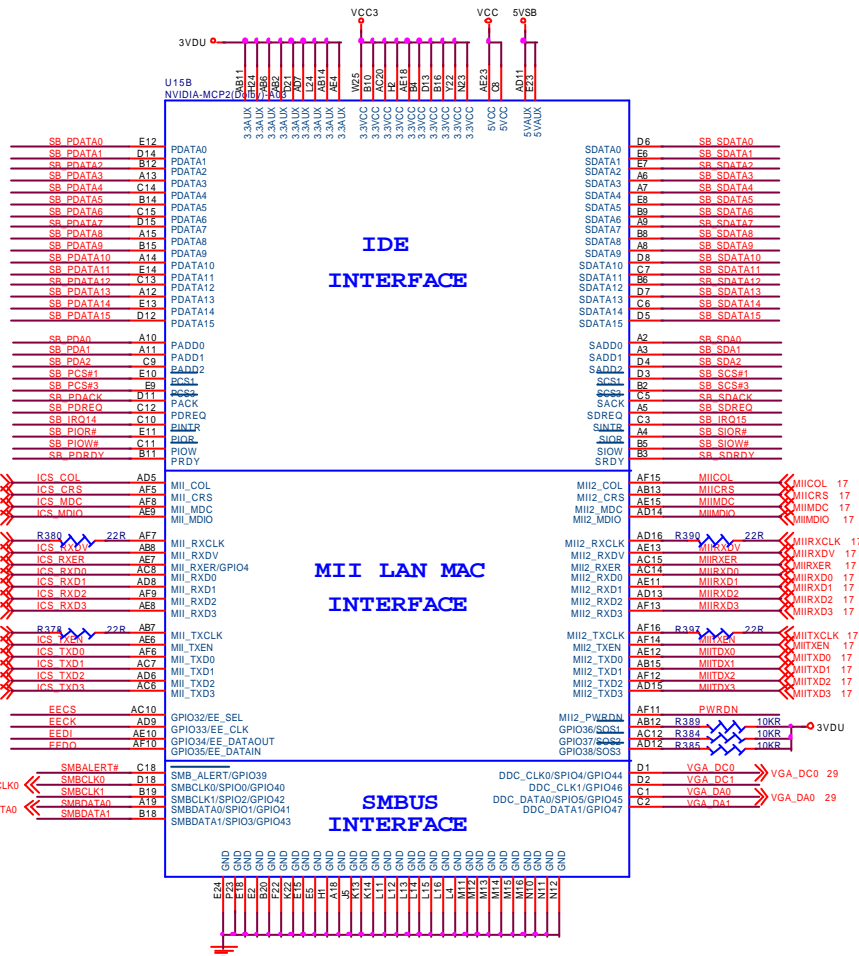






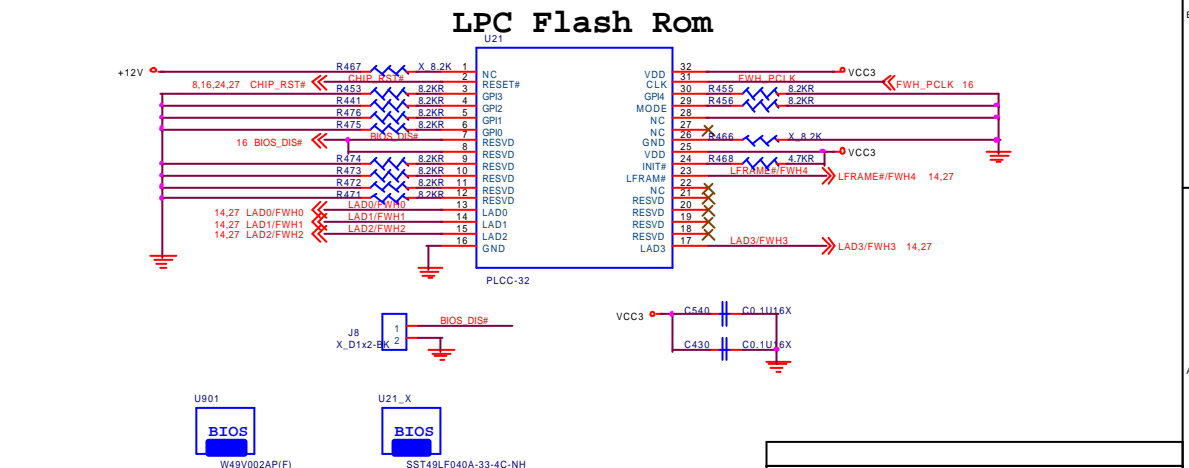
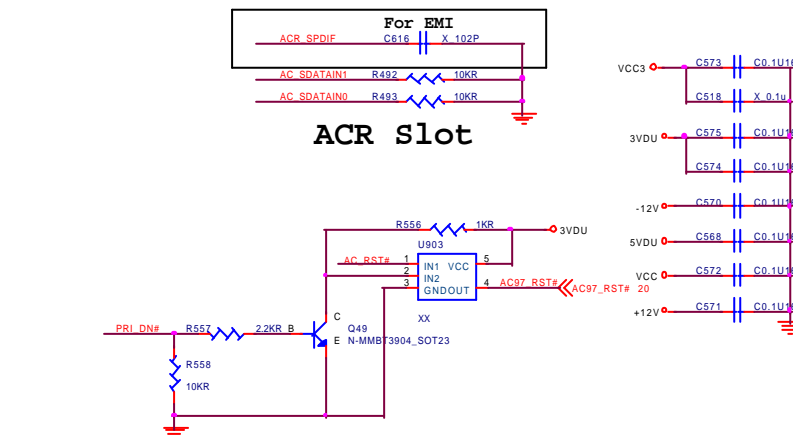


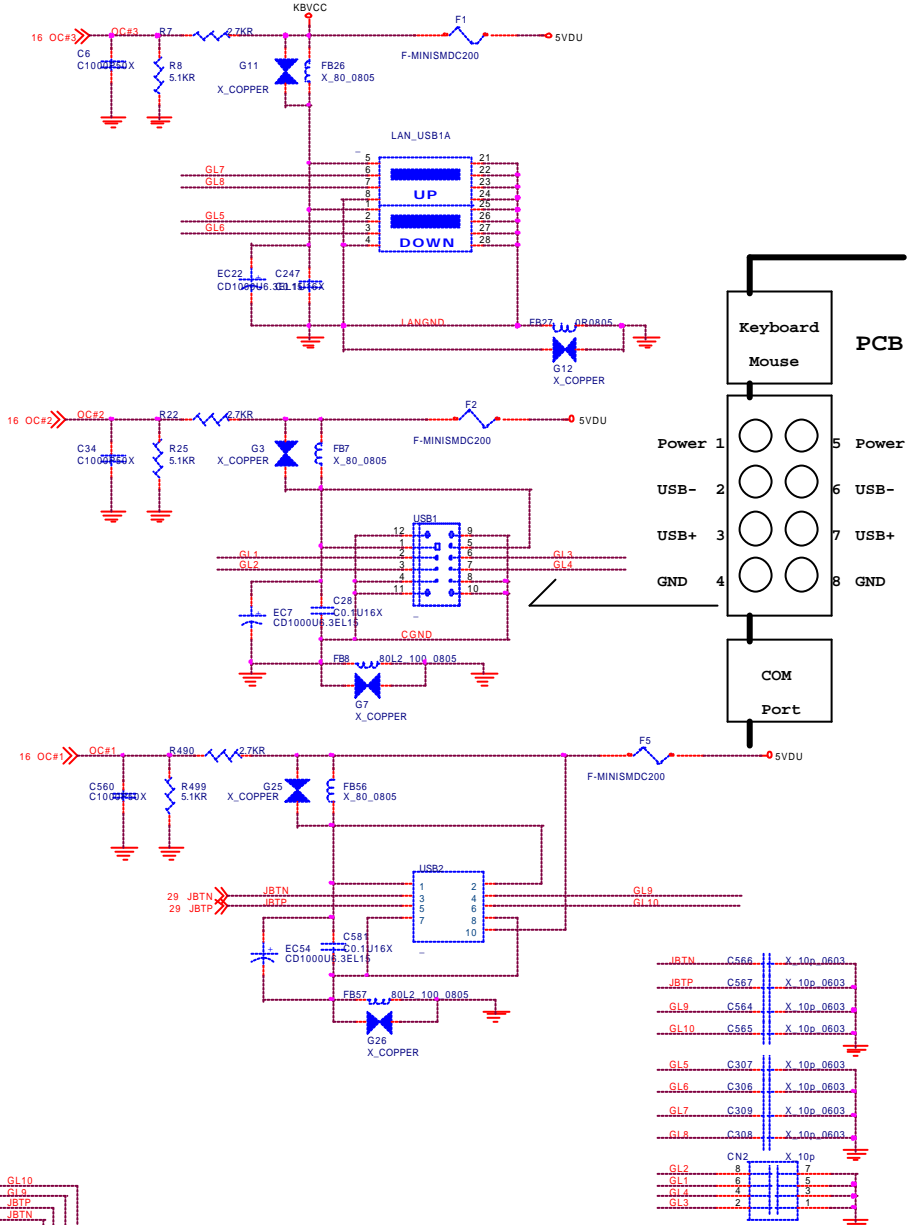
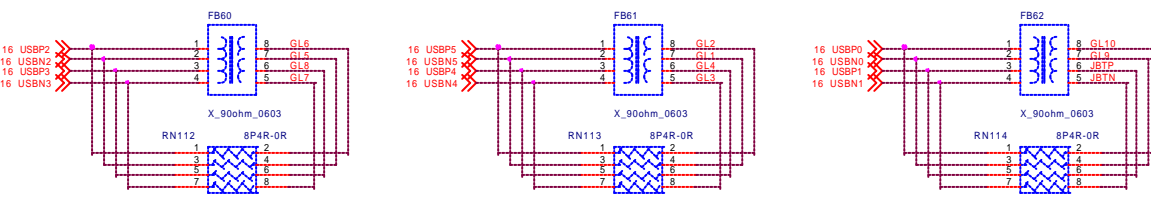
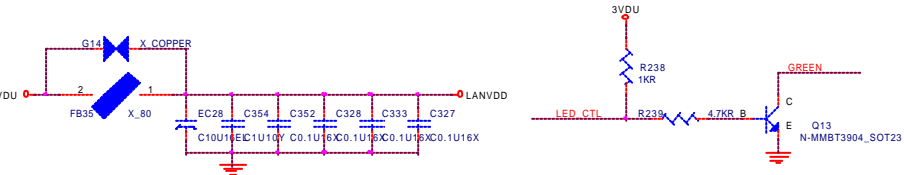
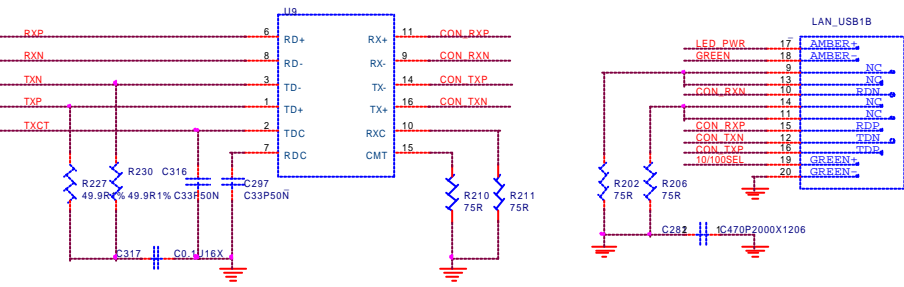
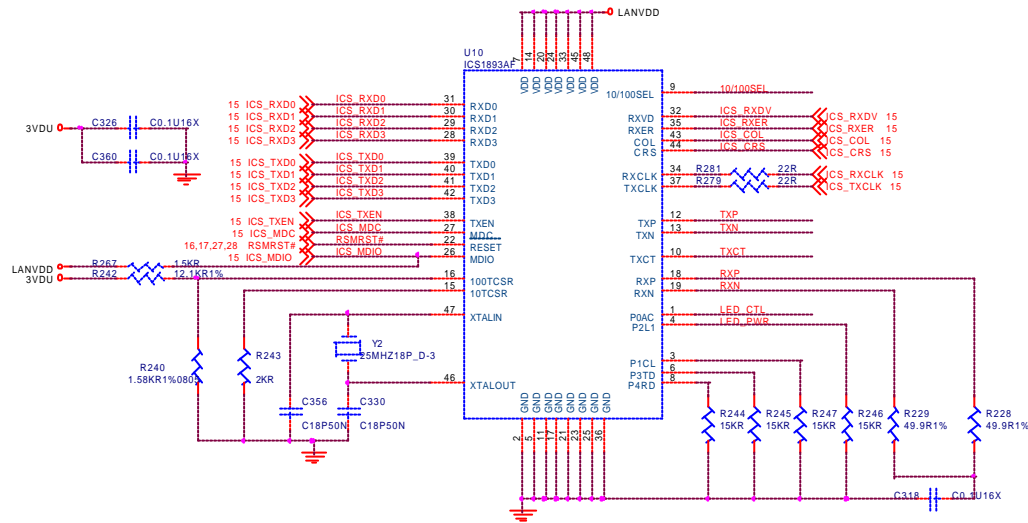










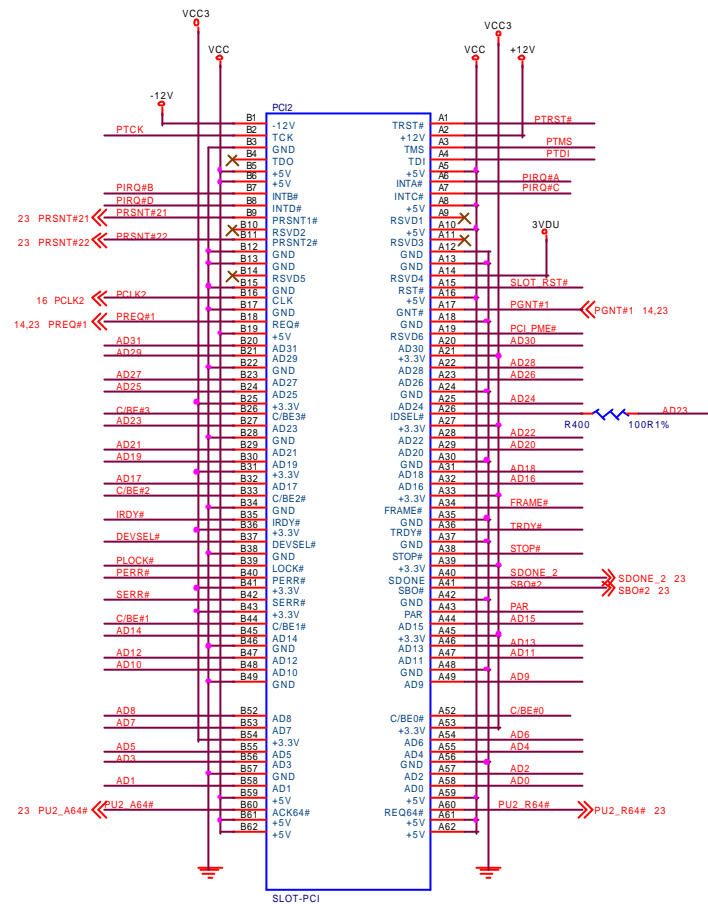
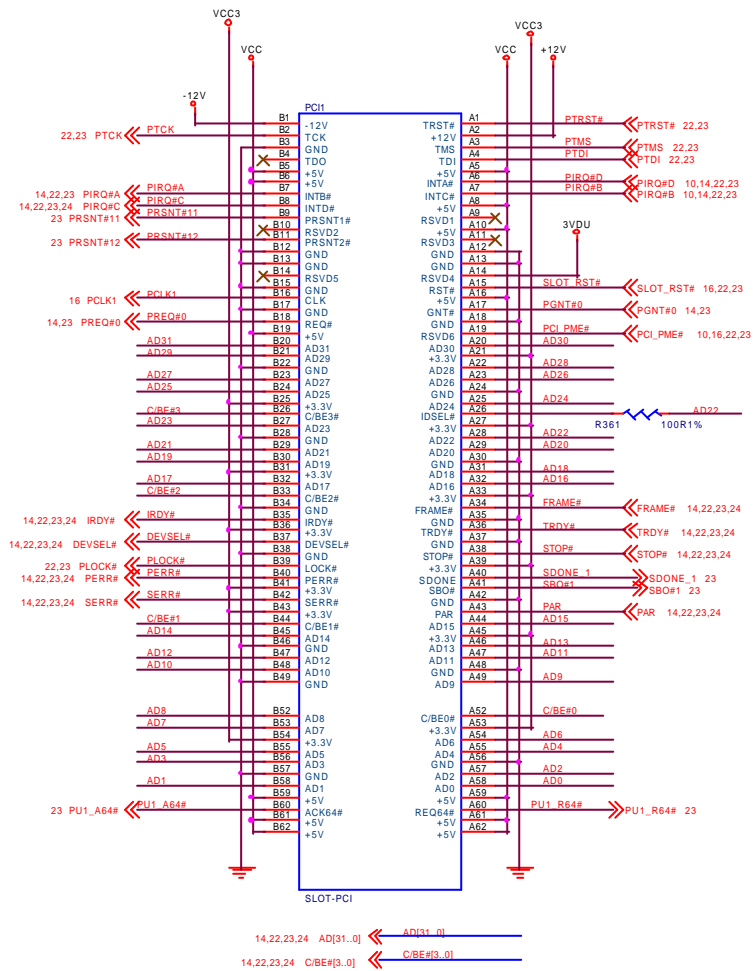


For EMI only

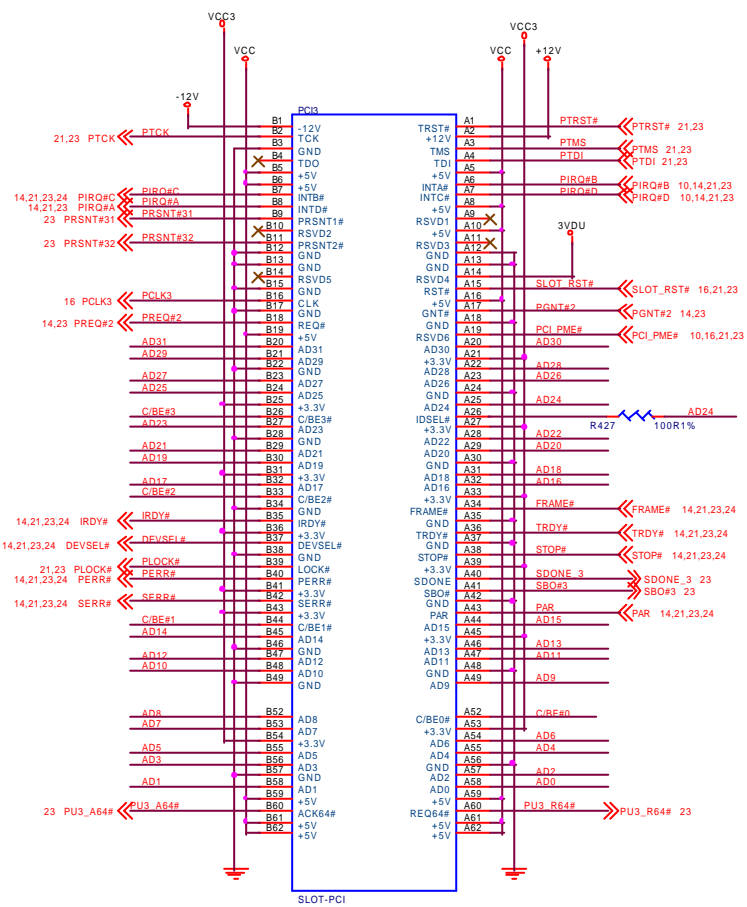
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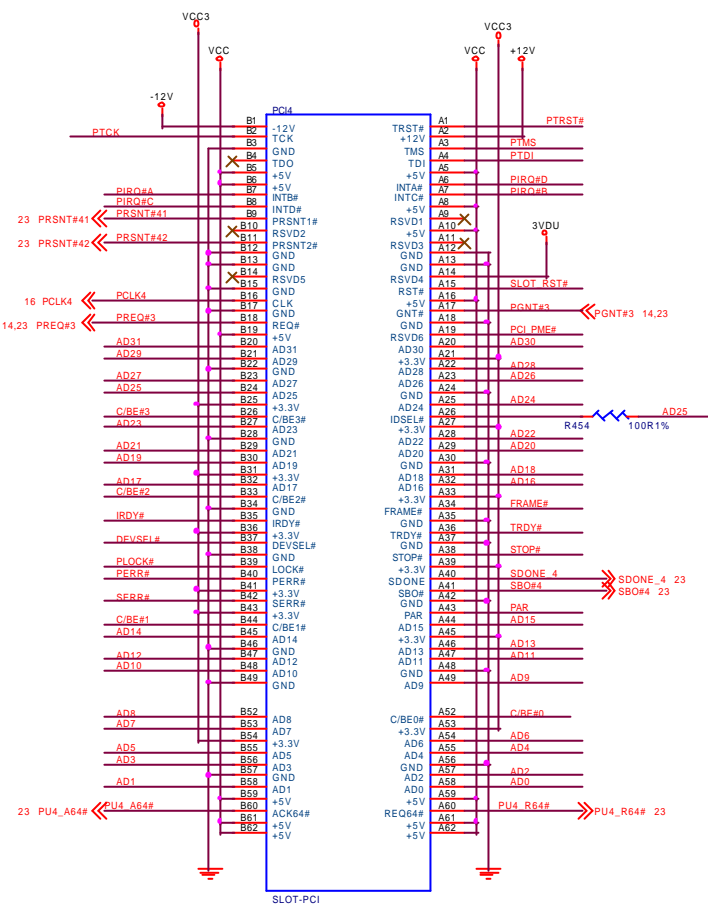




Title		
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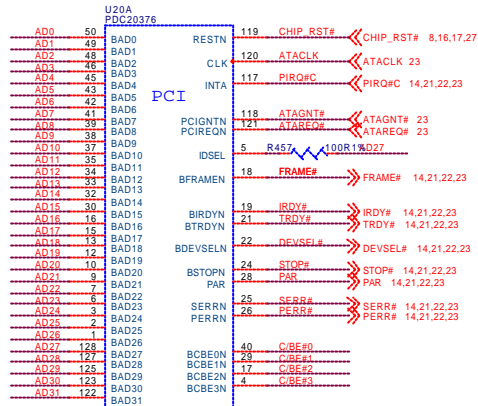


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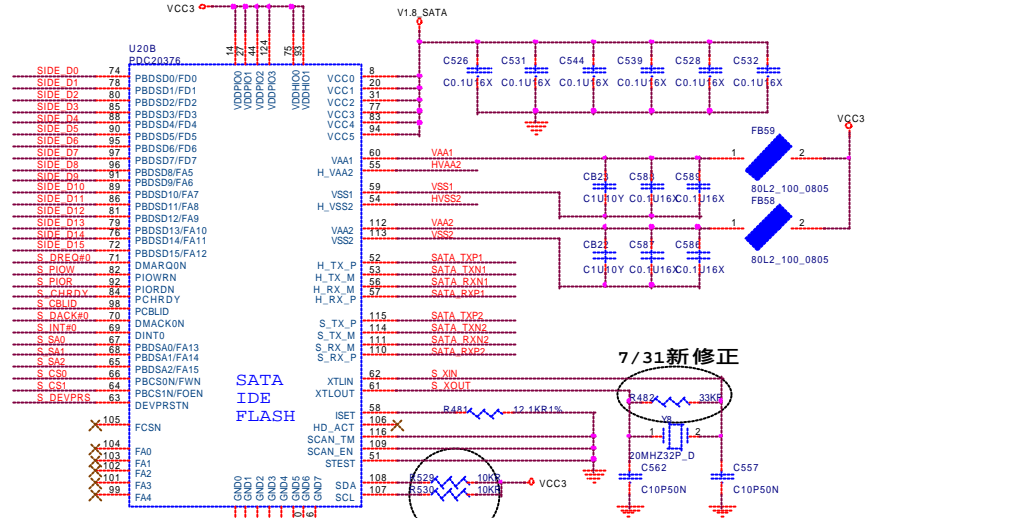


Title		
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PCI Slot 3 & 4		0D
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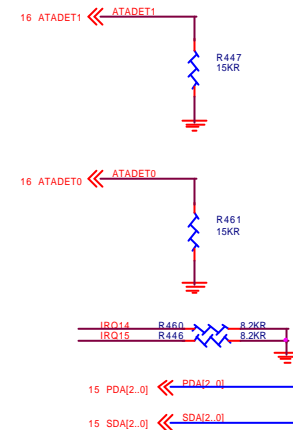
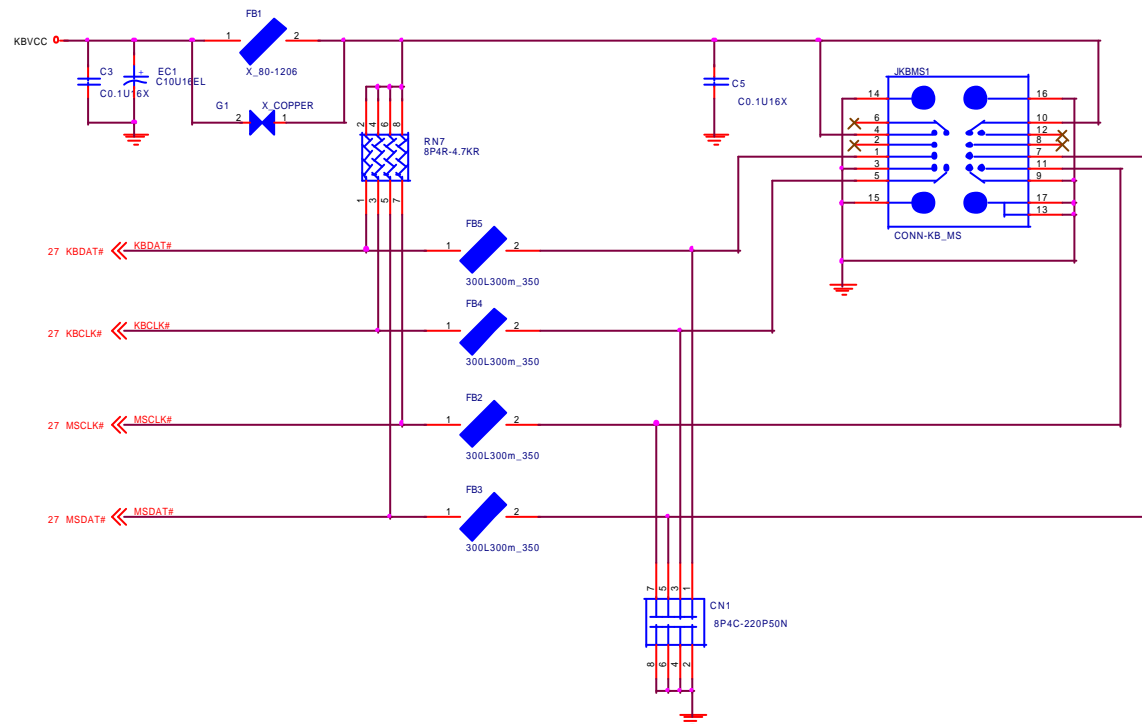
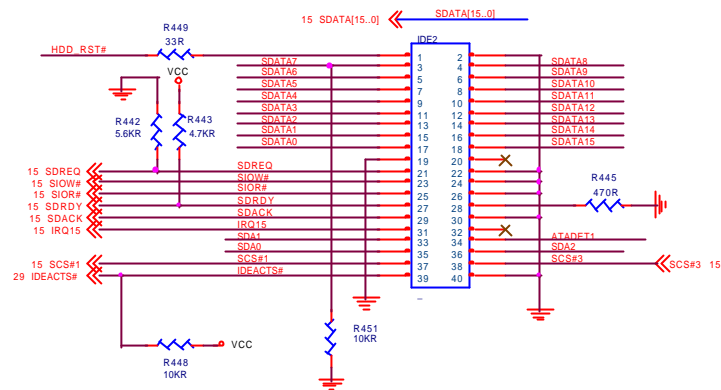
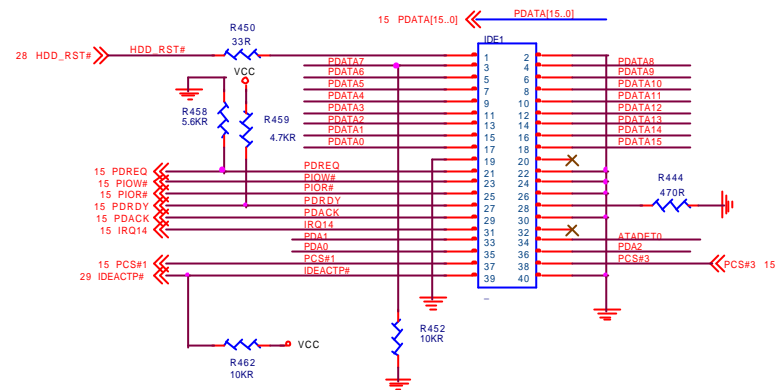


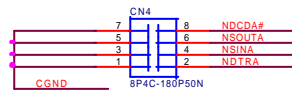
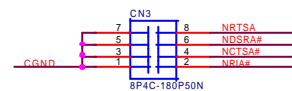
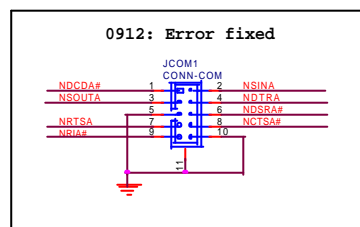
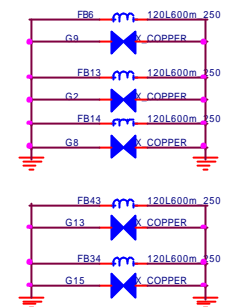
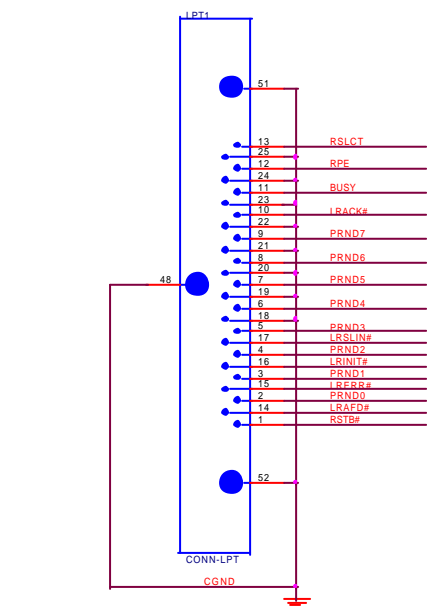


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14,21,22,23 C/BE[3:0] << C/BE[3:0]

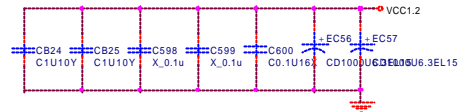




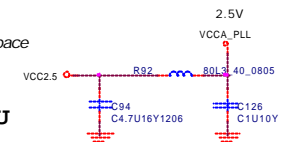








25 mils Trace / 12 mils Space



Closed to CPU

## 運算放大器分析

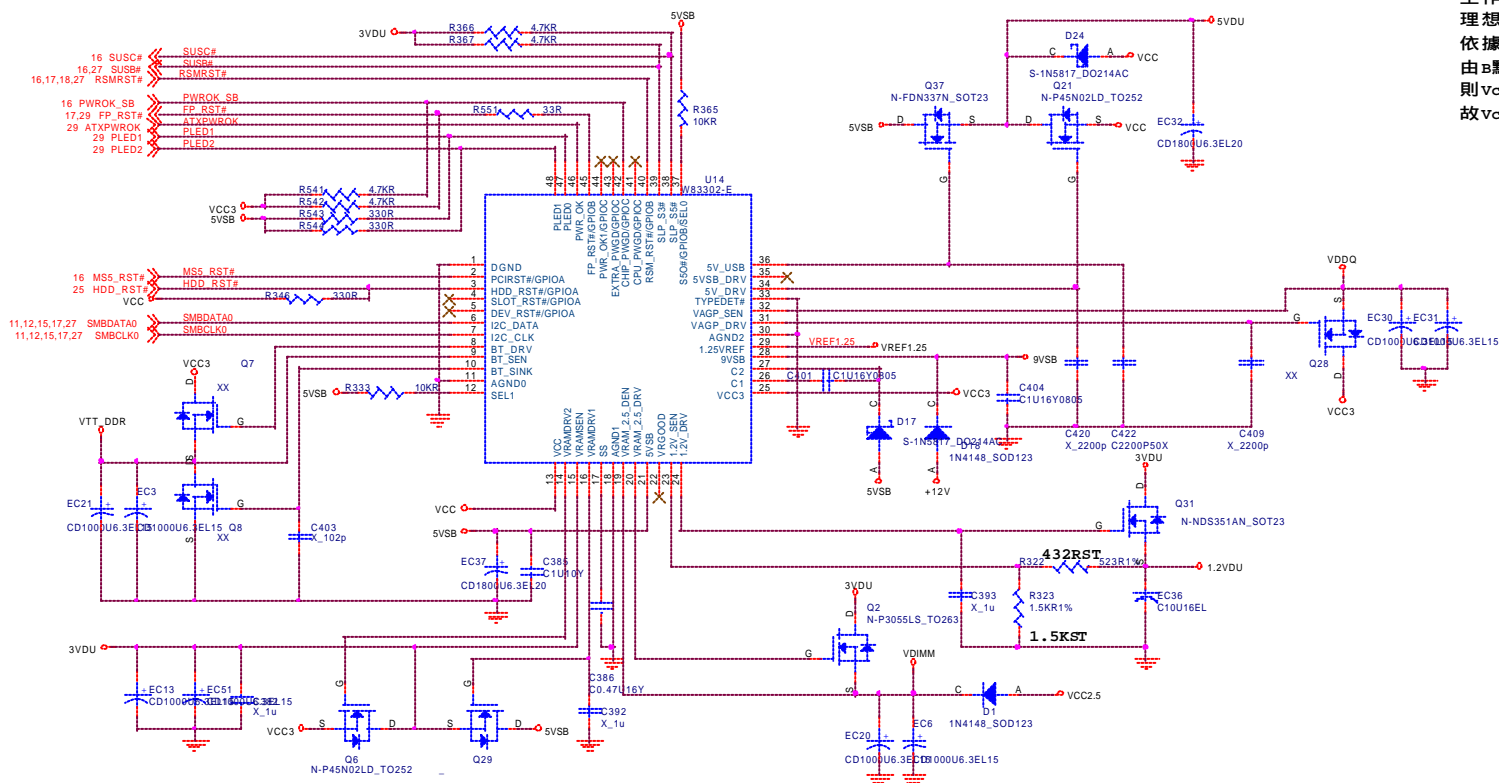
工作原理：藉由B點的回授，運算放大器可決定MOSFET的導通量。  
理想運算放大器中，跨在+,-兩端隻電壓為0V。

依據KVL,  $-V_{ba} + V_{bc} + V_c = 0$  或  $V_{bc} = V_{ba} - V_c = V_1 - V_c$

由B點的KCL可得： $(V1/R55)+[(V1-Vc)/R48]=0$

則  $V_C = uV_1$ ,  $u = 1 + (R_{48}/R_{55})$

故  $V_C = (1 + R_{48}/R_{55}) \cdot V_1$



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DATE	DESCRIPTION
4/16	1. First version initiated.

DATE	DESCRIPTION

DATE	DESCRIPTION
7/5 Rev.0B	Changed HSDIN#[0..1] form pull_down to pull_up Vcore
	Added J10 for user/safe mode selection
	Changed R203/R207 from pull_down to pull_up
	Added R507 where connected to VCCA_PLL
	Used VCCPLL_NB to control PWROK_SB delay
	Added pull_up resistor R526/R527 for PERR#/SERR# of AGP slot
	Swap APICD[0..1] for correct connection
	Added comm chock for USB 2.0
	IEEE1394 power circuit modified

DATE	DESCRIPTION
8/10 Rev.0C	Removed power sequence control circuit.
	Added FSB 166MHz circuit for new CPU.
	Changed DDR serial resistor from 0603 to 0402.
	Changed chipset core power to 1.6V@7A

DATE	DESCRIPTION
10/2 Rev.0D	COM port footprint fixed.
	Changed FSB 166MHz circuit for Nvidia chipset.
	Changed CPU power good circuit for Nvidia chipset.
	DDR serial resietor removed.

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